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Strategies for braiding and ground state preparation in digital quantum hardware

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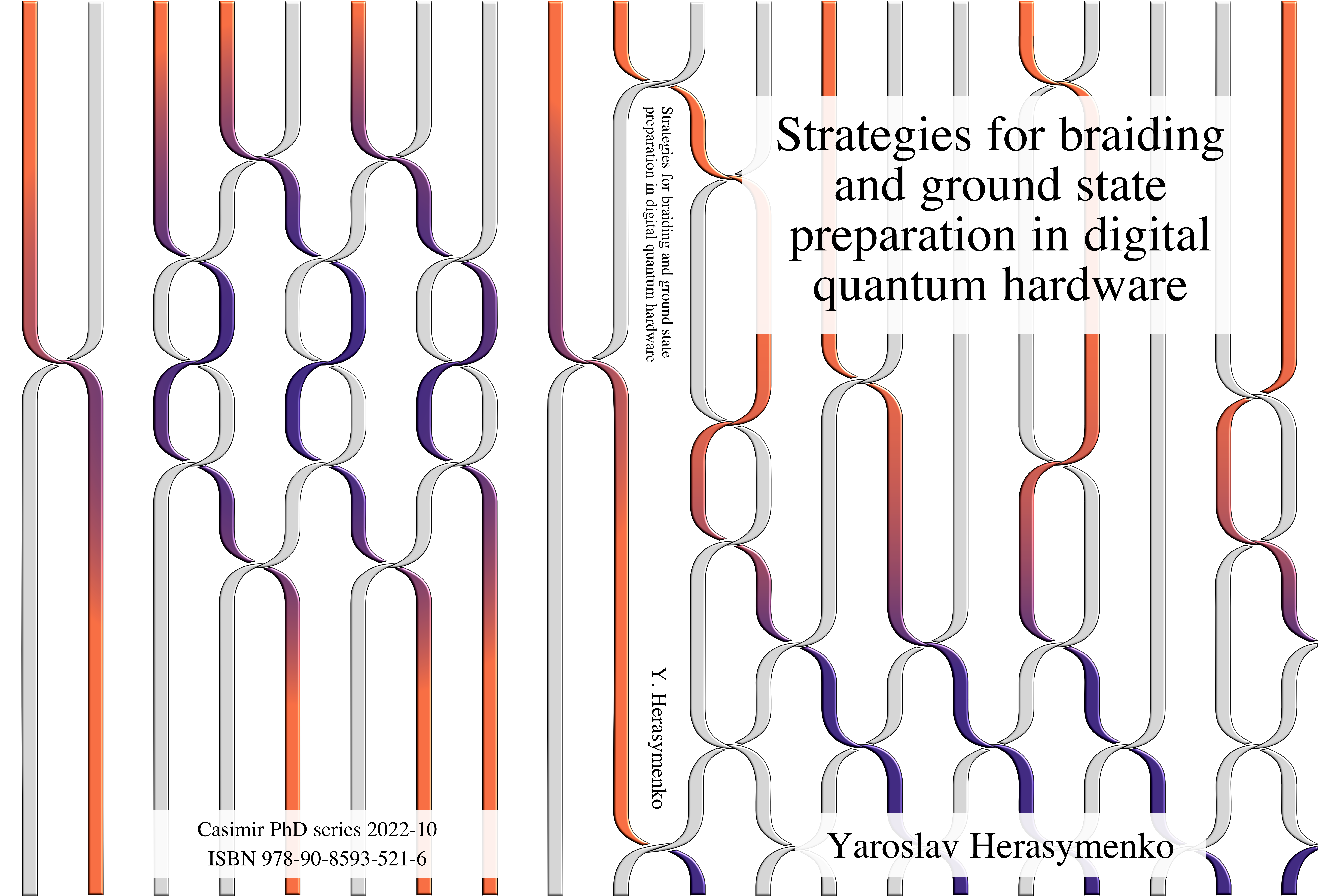
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