

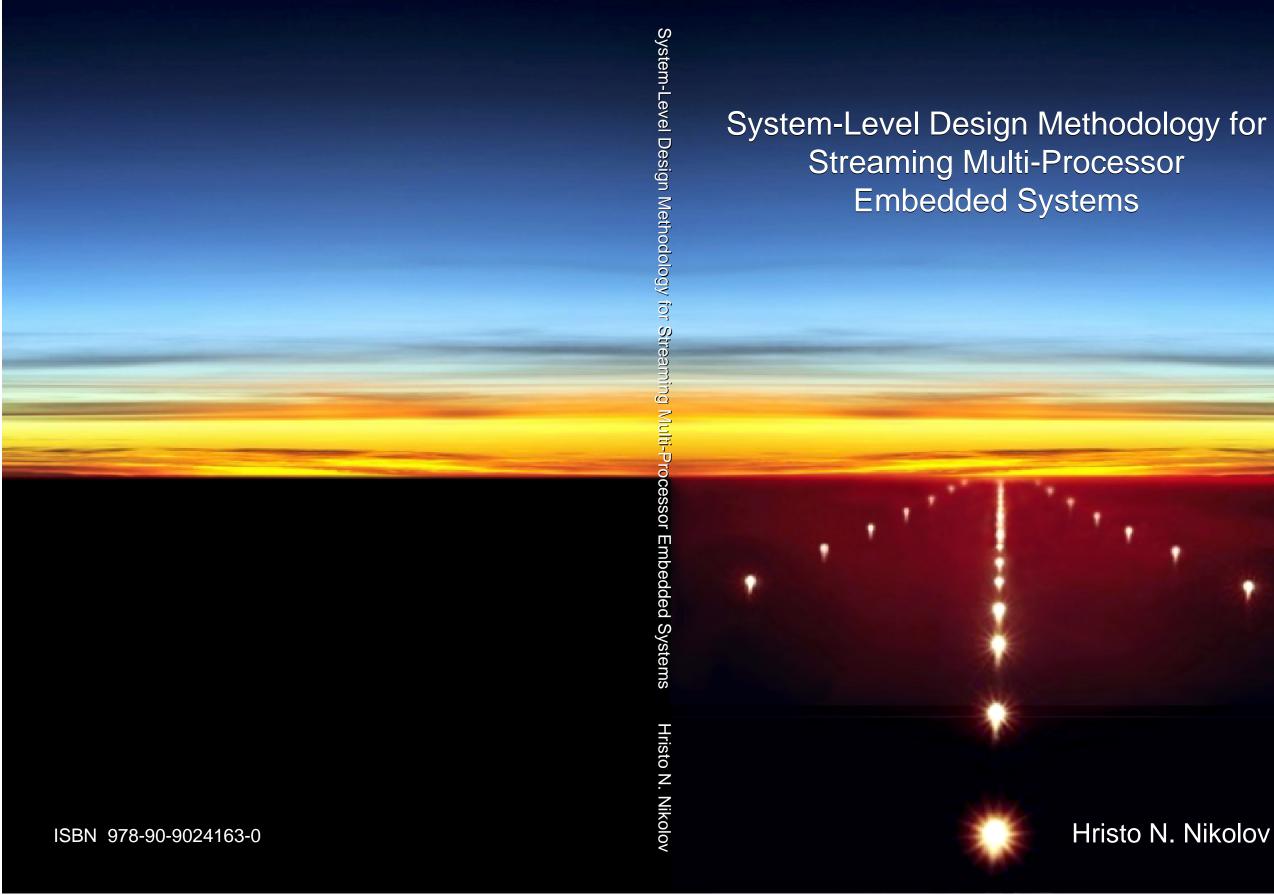
## **System-level design methodology for streaming multi-processor embedded systems** Nikolov, H.N.

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