# **On the Formation of a Conducting Surface Channel by Ionic-Liquid Gating of an Insulator**

*Hasan Atesci, Francesco Coneri, Maarten Leeuwenhoek, Jouri Bommer, James R. T. Seddon, Hans Hilgenkamp, and Jan M. Van Ruitenbeek\**

**Ionic-liquid gating has become a popular tool for tuning the charge carrier** densities of complex oxides. Among these, the band insulator SrTiO<sub>3</sub> is one of **the most extensively studied materials. While experiments have succeeded in inducing (super)conductivity, the process by which ionic-liquid gating turns this insulator into a conductor is still under scrutiny. Recent experiments have suggested an electrochemical rather than electrostatic origin of the induced charge carriers. Here, experiments probing the time evolution of conduction of SrTiO3 near the glass transition temperature of the ionic liquid are reported. By cooling down to temperatures near the glass transition of the ionic liquid, the process develops slowly and can be seen to evolve in time. The experiments reveal a process characterized by waiting times that can be as long as several minutes preceding a sudden appearance of conduction. For the conditions applied in our experiments, a consistent interpretation in terms of an electrostatic mechanism for the formation of a conducting path at** the surface of SrTiO<sub>3</sub> is found. The mechanism by which the conducting **surface channel develops relies on a nearly homogeneous lowering of the** surface potential until the conduction band edge of SrTiO<sub>3</sub> reaches the Fermi **level of the electrodes.**

# **1. Introduction**

The carrier density in materials is the central factor in all electron transport properties. Controlling this carrier density by externally applied gate potentials permits the study of transport and electron–electron interaction effects as a continuous function of this density. Applying an electrostatic potential by means

Dr. F. Coneri, J. Bommer, Dr. J. R. T. Seddon, Prof. H. Hilgenkamp MESA+ Institute for Nanotechnology

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of metallic gates separated by a dielectric from the material under study allows to cover only a limited range of carrier densities. Using ionic liquids (ILs) or ionic gels as dynamic dielectrics between the gate and the device gives access to a much wider range of carrier densities.<sup>[1-3]</sup> When a potential difference is applied across the IL, the entire potential drop is concentrated at the IL/electrode surfaces, where electric double layers (EDLs) are formed between a sheet of ions of one dominant polarity and a sheet of induced charges just below the surface of the material. These charge layers are separated by a distance as small as 1 nm, thereby producing extremely large electric fields. This makes it possible to induce carrier densities as high as  $8 \times 10^{14}$  cm<sup>-2</sup> at the liquid/solid interface for a material that is otherwise a deep insulator.[1] These carrier densities are large enough to permit controlling fundamental phenomena such

as magnetic order, phase transitions, and superconductivity by means of a gate potential.[2,4–8]

Among the materials used for such studies strontium titanate, SrTiO<sub>3</sub>, stands out as one of the most widely studied and best characterized systems.[9] It is a band insulator with an indirect bandgap of 3.25 eV, which can be converted into a good conductor at its surface, and even into a superconductor.<sup>[10]</sup> Disorder plays a role, and at low densities the conductance is best described by variable range hopping and the formation of percolation paths for the electrons in the disorder potential.[11] The origin of this disorder could be intrinsic to  $SrTiO<sub>3</sub>,<sup>[9]</sup>$  or it may result from density fluctuations in the ionic liquid.[12,13] The role of the ionic liquid also comes into play when discussing the nature of gating in terms of a purely electrostatic effect, or (partially) as a result of electrochemical modifications of the surface. For sufficiently strong electric fields and field gradients at the interface one should anticipate inducing disorder and chemical modifications at the surface. Indeed, experiments have shown the influence of IL gating on the oxygen content of complex oxides,  $[14-16]$  while others have shown an unusually high buildup of charges,[17] characteristic of induced electrochemical reactions. For the material of interest here,  $SrTiO<sub>3</sub>$ , several reports have given conflicting views on the mechanism of IL gating being either electrochemical<sup>[15]</sup> or electrostatic<sup>[3,10,12]</sup> in origin.

H. Atesci, M. Leeuwenhoek, Prof. J. M. Van Ruitenbeek Huygens-Kamerlingh Onnes Laboratorium Leiden University

P.O. Box 9504, 2300 RA Leiden, The Netherlands E-mail: ruitenbeek@physics.leidenuniv.nl

University of Twente P.O. Box 217, 7500 AE Enschede, The Netherlands

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More recently, the dynamics of the formation of conducting surface channels has received attention from several groups.<sup>[18-20]</sup> The standard descriptions borrowed from electrochemistry, in terms of homogeneous Helmholtz or Gouy–Chapman layers at the interface with the solid, may not be applicable.[19] A concrete model of the dynamic evolution of a conducting channel was proposed by Tsuchiya et al.,<sup>[18]</sup> in terms of a gradual spreading of the conducting channel over the surface of the solid, starting from the source and drain contacts. Here we address the question of how the EDL forms on an insulator. We therefore seek to investigate the development and underlying mechanism of conductivity across the surface of  $SrTiO<sub>3</sub>$  in the time domain. To this end, we slow down the gating process by lowering the temperature at which the gate potential is applied, to temperatures just above the glass transition of the ionic liquid. This allows us to follow the process of gating in time and elucidate the role of the ionic liquid in the process. We were further motivated to investigate the charging dynamics at low temperatures because under such conditions unwanted electrochemical processes are more easily suppressed. Our observations show that, at variance with the interpretation by Tsuchiya et al.,<sup>[18]</sup> the surface conducting channel on SrTiO<sub>3</sub> develops homogeneously. The observed time delay of conductance after switching on the gate potential is attributed to the time required for bringing the surface potential to the conduction band edge of  $SrTiO<sub>3</sub>$ . The subsequent evolution of the conductance of the channel is consistent with apercolation description of the 2D electron system.

## **2. Experimental Section**

All of the sample processing was done on pristine undoped SrTiO<sub>3</sub> (001) crystal substrates with a miscut angle of  $< 0.1$ degrees, as obtained from Crystec GmbH. The contacts to the  $SrTiO<sub>3</sub>$  surface were defined by means of photolithography, as is shown in **Figure 1**a. After development of the photoresist, the patches on the surface onto which the electrodes were to be structured were first treated by Ar ion milling (500 V, 0.4 Pa), such as to ensure low-resistance ohmic contacts between the metal electrodes and the SrTiO<sub>3</sub> surface. After this process, layers of 6 nm Ti and 150 nm Au were deposited by sputtering. In order to reduce leakage currents, in a second photolithography step the sample surface was covered by a separator layer (SL) in the form of an insulating photoresist (purple in Figure 1a), except for the electrical contact pads, the gate electrode, and the surface channel area. Chemical cleaning in an oxygen plasma (10 Pa, 13 W) was used for removing any residuals of the photolithographic process on the bare  $SrTiO<sub>3</sub>$  surface. The resulting surfaces present flat terraces with step heights corresponding to the SrTiO<sub>3</sub> unit cell parameter of 0.391 nm, as was verified by atomic force microscopy (AFM), Figure 1b. We have tested patterns defined on the chip consisting of a single channel with multiple contacts, permitting four-probe measurements and voltage measurements transverse to the current direction (Layout 1, see **Figure 2**a), as well as patterns defining many chan nels of various lengths in two-probe configurations. Typically a channel is  $20 \mu m$  wide and between 10 and 500  $\mu$ m in length (Layout 2, see Figure 2b).

After fabrication, the sample was placed inside a glovebox under pure  $N_2$  atmosphere (<0.1 ppm  $O_2$ , H<sub>2</sub>O), where it was



Figure 1. a) A schematic cross section of the sample. Yellow and blue spheres represent TFSI(-) and DEME(+) ions, respectively. The gate (G), source (S), and drain (D) electrodes have a thickness of 6 nm Ti plus 150 nm Au. For the areas onto which the Ti/Au layer is deposited, the  $SrTiO<sub>3</sub>$  is ion milled to about 100 nm depth. The separator layer (SL) is 1.7  $\mu$ m thick. b) AFM image of a typical SrTiO<sub>3</sub> surface after an oxygen plasma treatment. Clear terrace steps can be observed. The inset shows the profile indicated by the black line, showing step heights corresponding to the SrTiO<sub>3</sub> unit cell height.

heated to 120°C for 1 h in order to remove adsorbates from the surface. The ionic liquid used here is *N*,*N*-diethyl-*N*-(2-methoxyethyl)-*N*-methylammonium bis(trifluoromethylsulphonyl)- imide (DEME-TFSI), which was kept stored in a closed bottle. Special care was taken in keeping the ionic liquid free from oxygen and water as much as possible. Before use, the bottle was opened in the glove box and the IL was pretreated by heating it to 60°C for a period of 72 h. A metal needle was then inserted into the bottle in order to pick up a small droplet at the tip of the needle. This droplet was applied on top of the sample so as to cover the Au gate electrode and the channel area, as illustrated schematically in Figure 2c, followed by the transportation of the sample to the sample chamber of the Oxford Instruments Cryofree Teslatron cryogenic measurement system. Before starting the measurements this chamber was thoroughly flushed with high-purity He gas and pumped to 10−<sup>4</sup> Pa to evacuate contaminations from the atmosphere before cool down of the sample.



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**Figure 2.** a) Schematic representation of the electrodes and channel of Layout 1, consisting of one channel of dimensions  $60 \times 300 \mu m^2$ . b) The same for Layout 2, consisting of seven channels of the same width (20  $\mu$ m) and varying lengths (5, 10, 20, 50, 100, 200, and 500  $\mu$ m). c) A schematic top view of the sample designed according to Layout 2. Except for the large Au gate electrode around the center of the sample, the Au contacts at the sides of the samples, and the channel area near the center, the whole surface is covered by a hard-baked photoresist layer (purple). For the pattern illustrated here, the contacts at the sides are connected to a Hall bar configuration for measuring the electronic properties of the channel area of SrTiO<sub>3</sub>. A bias voltage V<sub>sd</sub> between source and drain electrodes is applied, while simultaneously measuring the source-drain current *I*sd. The gate electrode (G), has an exposed area of more than 100 times the area of the exposed SrTiO<sub>3</sub> channel. The potential difference  $V_g$  is applied between the gate and drain electrodes, while the gate current *I*<sup>g</sup> is measured simultaneously.

We have used two Keithley 2400 SourceMeters for the experiments, one of which was used for setting up a source–drain bias voltage of  $V_{sd} \leq 250$  mV while simultaneously measuring the source–drain current *I*<sub>sd</sub>. We verified that the results are independent of this setting using compliance values as low as 20 mV. The initial, insulating state of  $SrTiO<sub>3</sub>$  has a resistance above our measurement limit of 25 G $\Omega.$  The other SourceMeter was used for applying the desired gate voltage  $V_g$ , and for simultaneously monitoring the gate current  $I_{\varphi}$ .

We have tested only positive gate voltages, leading to electron doping of the  $SrTiO<sub>3</sub>$  surface, in accordance with previous experiments. In order to minimize electrochemical carrier doping of the channel the gate voltages  $V_{\rm g}$  were limited to a maximum of 3.2  $V^{[16]}$  Furthermore, the temperatures at which the gate potential was first applied, which we will refer to as the charging temperatures, were chosen to be close to the glass transition temperature  $T_{\rm g} \approx 182$  K of DEME-TFSI.<sup>[21]</sup>



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Figure 3. Time evolution of the source-drain current for a channel, 10  $\times$ 20  $\mu$ m<sup>2</sup>, Layout 2, at a bias voltage of  $V_{sd} = 250$  mV. After stabilizing the temperature at  $T = 195$  K and at  $V_g = 0$  the gate voltage  $V_g$  is switched at  $t = 0$  instantaneously to the values indicated by the labels for each of the curves. A time  $t_d$  elapses before the current suddenly rises above the noise floor of about 10 pA, and the current increases nearly exponentially.

#### **3. Experimental Results**

We start the experiments by stabilizing the sample at a temperature *T* above the glass temperature of the IL, after which at  $t = 0$ the gate potential  $V_g$  is switched from zero to a chosen fixed value, and the gate current is recorded simultaneously with the channel conductance. We observe the following: After switching the gate potential, a waiting time  $t_d$  elapses before we observe any measurable response in the conductance of  $SrTiO<sub>3</sub>$ , as shown in **Figure 3** for  $T = 195$  K and several settings of  $V_g^{(22)}$  When the order is switched back to zero the decay of the conductance takes gate is switched back to zero the decay of the conductance takes place on a much shorter time scale. We introduce a waiting time of at least 10 min before a new gate voltage is set.

The delay time of the main signal strongly increases for lower temperatures (as illustrated for a different sample in **Figure 4**, Layout 2) and becomes very long (>1000 s) when the temperature approaches the glass transition temperature of about 182 K of the ionic liquid. After the conductivity onset, the current approaches a nearly exponential increase, as shown by the blue dotted curves in Figure 4a. The current  $I_{sd}(t)$ , here plotted on a linear scale, is closely described by a single exponential function  $I_{sd}(t) = I_0(1 - \exp(-(t - t_d)/\tau))$ , which defines our time constants  $t_d$  and  $\tau$ . These time constants are plotted as a function of temperature in Figure 4b, showing that the times rapidly grow for temperatures approaching the glass transition, and that the two times are of similar magnitude.

The gate current, on the other hand, switches instantaneously, followed by a smooth decay, as shown in **Figure 5**. We have scaled each of the gate currents by the applied gate potentials, ranging from 1.4 to 3.0 V, and the resulting curves perfectly collapse to a universal time dependence. The lack of voltage dependence in the scaled gate current provides strong support for a purely electrostatic charging process. The curves in Figure 5 are closely described by a combination of two exponentially decreasing functions, with time constants  $\tau_{g1}$  and  $\tau_{g2}$ , as illustrated by the **CIENCE NEWS** 

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Figure 4. Time delay in the response of the conductance of a SrTiO<sub>3</sub> channel (10  $\times$  20  $\mu$ m<sup>2</sup>, Layout 2) upon application of a gate potential. a) Source–drain current as a function of time after switching to a fixed gate voltage as indicated, for a charging temperatures of 205 K. The blue dotted curves show the single-exponential fits (see text). The green curves give the fits based on the percolation model. The inset shows the source– drain current on a log scale for a range of temperatures and for  $V_g = 2.5$  V. b) Time delay  $t_d$  (blue) and time constants  $\tau$  (red) as obtained from the fits to the time evolution of the source–drain current in (a) for  $V_g = 2.5$  V, plotted as a function of the charging temperature. For a few temperatures we also show the discharge time constants, which are much faster. The green points represent the time constant  $\tau_{g2}$  for the decay of the gate current. The time constants increase nearly exponentially when approaching the glass temperature of the ionic liquid at 182 K, as illustrated by the straight dashed lines. The inset shows that the time constants are nearly independent of the gate voltage, for  $T = 205$  K, where we use the same color coding as in the main panel.

red broken curve. The absence of long-time tails in the gate current with  $\sqrt{t}$  dependence further testifies for the absence of electrochemical contributions to the charging process.[16] We find that the precautions taken to prevent oxygen and water from interacting with the ionic liquid are critical for this result. For example, when pumping the sample chamber of the cryogenic system to only 1 Pa we observe strong deviations from purely exponential decay of the gate current, the scaling of the gate current with gate voltage breaks down (Figure 5 inset), and the saturation value of the source–drain current becomes only weakly dependent on the gate potential.

The time constant  $\tau_{22}$  is also shown in Figure 4b, and has a similar magnitude and temperature dependence as found for  $t_d$ and  $\tau$ . The time constants are nearly independent of the gate potential to within 20%, as shown in the inset.

#### **4. Discussion**

The fact that all time constants,  $t_d$ ,  $\tau$ , and  $\tau_{g2}$ , have very similar values and that they depend in the same way on temperature sug-



**Figure 5.** Time evolution of the gate current *I*g, scaled by the gate potential, upon application of 15 different gate potentials ranging from 1.4 to 3.0 V at a charging temperature of 205 K. The curves perfectly collapse to a single smooth time evolution, which is closely described by a combination of two exponential functions with time constants  $\tau_{g1}$  and  $\tau_{g2}$  (red broken curve). The inset shows the curves between 2.3 and 3.0 V at a charging temperature of 220 K, typical of experiments performed at a minimum pressure of 1 Pa. In such circumstances, the curves are no longer described by a universal time dependence, and show clear *V*<sup>g</sup> dependence. The individual curves are better described with a Faradaic term. The red, broken curve is a single  $t^{-1/2}$  term fit of the data belonging to  $V_g = 3.0$  V  $(R^2 = 0.9814).$ 

gests that the processes are intimately linked, and are dominated by the ionic conduction in the liquid. The ionic conductivity becomes very small close to the glass transition and limits the charging time of the total capacitance C<sub>tot</sub>. The ionic conductivity can be described by the Vogel–Fulcher–Tammann (VFT) equation<sup>[23]</sup>

$$
\rho = A\sqrt{T} \exp \frac{B}{T - T_0} \tag{1}
$$

Here, *A* and *B* are constants related to the ion density and the activation energy for ion transport, respectively, and  $T_0$  is the ideal glass transition temperature. The temperature dependence of the time constants in Figure 4b agrees with this nearly exponential dependence, as indicated by the broken lines in the figure, although the temperature interval explored here is not wide enough for testing the full expression.

The time delay in the source–drain current *I<sub>sd</sub>* and the fact that the gate current in Figure 5 does not show a single-exponential decay as expected for the simple charging of a capacitor could be due to the charging properties of the IL. Yuan et al.<sup>[24]</sup> have analyzed the dynamic capacitance of the system of the DEME-TFSI ionic liquid in contact with a ZnO surface as a function of temperature and frequency. They find that the system can be represented by two resistor–capacitor systems in series. One of these represents the charging of the surface capacitance, and the other the geometric response of the ionic liquid. However, the latter produces a response on time scales that are two or three orders of magnitude shorter than those observed for the initial fast decay of the gate current, as can be read from Figure 5 in ref. [24] and such processes, if present, would not be resolved on the time scale of our experiments. In our case the two time scales find a natural explanation in terms of the voltage dependence of the surface capacitance of SrTiO<sub>3</sub>.

Given the fact that  $SrTiO<sub>3</sub>$  is a wide bandgap insulator the delay time in the source–drain current *I*<sub>sd</sub> can be attributed to the role of the threshold potential *V*<sub>th</sub> required for bringing the electrochemical potential at the surface of  $SrTiO<sub>3</sub>$  to the edge of the conduction band. After switching on the gate voltage, the potential at the  $SrTiO<sub>3</sub>$  surface is expected to rise exponentially in time. At equilibrium, the potential in the ionic liquid near the surface of  $SrTiO<sub>3</sub>$  is given by the division of the potential over the series connection of the capacitance at the gate and the capacitance to the outside world. Since the former is orders of magnitude larger, the potential at the surface reaches nearly the full gate potential. In the process of building up this surface potential as a function of time the conduction band of  $SrTiO<sub>3</sub>$  is locally pulled down until, at a threshold potential *V*<sub>th</sub>, the chemical potential of the electrons in the Au leads is aligned with the bottom of the conduction band. This condition is met after a time delay  $t_d$ , and from this moment on a 2D electron system (2DES) can develop at the surface of  $SrTiO<sub>3</sub>$ . As the ionic surface charging continues toward saturation, the density of carriers in the 2DES grows and the conductivity increases. This interpretation of the delay time is consistent with the fact that we observe no substantial conductivity for gate voltages smaller than 1.8 V.

The integrated gate current after saturation gives the total charge *Q* and we find that this scales linearly with the gate potential, in agreement with the scaling in Figure 5. The proportionality constant between  $Q$  and  $V_g$  gives the total capacitance,  $C_{\text{tot}} = Q/V_{\text{g}}$ , for which we obtain 1.4 nF, for the sample configuration of the data in Figures 4 and 5. This capacitance is larger than expected for a capacitance associated with an individual channel. For the channel with size  $20 \times 10 \mu m^2$  used for the measurements presented above, and a specific capacitance at high gate voltage at the SrTiO3 surface of 13  $\mu$ F cm $^{-2},^{[18]}$  the expected capacitance is 26 pF. The larger value of the capacitance obtained from the gate current reflects the fact that the capacitance associated with the gate current is the total capacitance of the ionic liquid droplet to the outside world and includes the capacitive coupling to the other channels on the sample. Estimates based on the total exposed area of  $SrTiO<sub>3</sub>$  gives 2.3 nF, which agrees within a factor of two with observation.

The fact that the capacitance is dominated by the interface with  $SrTiO<sub>3</sub>$  implies that the capacitance must be voltage dependent. The delay time observed in the source–drain current implies that this capacitance switches to a larger value as soon as the potential at the surface brings the Fermi energy up to the conduction band edge. We take this effect into account by a simplified model through a combination of two time constants,  $I_{\rm g}(t) = I_1 \exp(-t/\tau_{\rm g1}) + I_2 \exp(-t/\tau_{\rm g2})$ , and the fit to this expression is shown by the broken curve in Figure 5. The second time constant is the one associated with the charging of the surface capacitance in the regime of a finite conducting surface charge density. The cross over between the two regimes occurs at a time that is similar to the delay time observed in the source–drain current. The initial time constant  $\tau_{g1}$  is attributed to the residual capacitance, dominated by the Au contacts to the channels.

The discharge curve for the source–drain current, recorded when switching the gate potential back to zero, is again closely described by a single exponential. This represents the relaxation of the EDL and we observe that this process is much faster than the build-up of the double layer, as shown in Figure 4b. As was



Figure 6. Plot of the ionic liquid resistivity as a function of temperature. The blue points are obtained in this work as calculated from the observed delay times. The red and pink points represent literature values for  $\rho$ , as taken from refs. [21] and [25]. The fit curve is based on the Vogel–Fulcher– Tammann equation. The fit parameters are:  $T_0 = 151 \pm 7$  K,  $A = 4.8 \times$  $10^{-3}$  Sm<sup>-1</sup> K<sup>-0.5</sup> and *B* = 1.3 × 10<sup>3</sup> K ( $R^2$  = 0.994).

discussed in ref. [20] the difference in charging and discharging times can be understood as the competition between two driving forces. When charging the EDL the cations are driven toward the interface by the electric field, but the concentration gradient drives them in the opposite direction. When discharging, the electric field of the charge layer works in the same direction as the concentration gradient, resulting in a larger combined driving force for the current. From these observations we conclude that the ionic conductivity obtained from the process of charging and discharging of the EDL may deviate from the bulk conductivity.

Nevertheless, we can use the experimental values for  $\tau$  for estimating the ionic resistivity  $\rho$  taking into account the geometry and size of the electrodes according to Layout 2. We take the channels as approximately 1D, and combine the contribution to the charging current from all channels on the sample. The resistivity can then be approximated by  $\rho = \pi R L / \ln(W/a)$ , where  $R = \tau / C$  is the resistance obtained from the time constant and the capacitance,  $L = 910 \mu m$  is the total length of the channels,<br> $a = 10 \mu m$  is half the width of the channels, and  $W \approx 1 \mu m$  is  $a = 10 \mu m$  is half the width of the channels, and  $W \approx 1$  mm is the radius of the ionic liquid droplet. The values for a thus obthe radius of the ionic liquid droplet. The values for  $\rho$  thus obtained are accurate to within a factor of two as limited by our knowledge of the geometry of the droplet (represented by the errorbars), and are plotted in **Figure 6** along with the known literature values.<sup>[21,25]</sup> The curve fit is based on the VFT Equation (1).<sup>[23]</sup> The fit parameter  $T_0$  is the ideal glass transition temperature, which is typically 30–50 K lower than the *<sup>T</sup>*<sup>g</sup> measured by means of differential scanning calorimetry, $[23]$  as is the case for DEME-TFSI,<sup>[21]</sup> In our case the VFT function gives a good fit, with  $T_0 = 151 \pm 7$  K, demonstrating that our interpretation of the delay time produces values for the ionic liquid conductivity that are in good agreement with the available literature data. Note that our data points appear to be slightly above an extrapolation of the literature values, which may result from the fact that our resistivity data are obtained from the properties of the EDL and thus is influenced by the concentration gradient.

In order to connect the charging/discharging dynamics in the ionic liquid with its effect on the electrical conductivity of the channel we now turn to the properties of the 2DES, notably its conductivity extrapolated to long times,  $\sigma_{\infty}$ . Despite the metallic characteristic at high charging voltages,[10] the 2DES cannot be viewed as a normal metal because we find that the conductivity strongly depends on the length of the channels, decreasing for channel lengths running from 10 to 500  $\mu$ m. As another indication of this anomalous character the conductivity of the 2DES remains well below the conductance quantum,  $\sigma_{\infty} \ll e^2/h$ . These observations are consistent with an interpretation of the conductivity in terms of Anderson localization and the formation of percolation networks of conducting paths.<sup>[11]</sup>

This observation appears to be contradicting the nearly perfect exponential growth of conductance with time seen in Figure 4. In a 2DES percolation model the conductance is expected to be controlled by the charge density  $n$  according to<sup>[26]</sup>

$$
\sigma = A (n - n_c)^{4/3} \tag{2}
$$

where *A* is a system specific constant and  $n_c$  is the 2D critical density for forming a percolation path. Assuming a sharp conduction band edge, the charge density at the surface is controlled by the local electrostatic potential *V* created by the ionic liquid at the surface of the  $SrTiO<sub>3</sub>$  crystal, as

$$
n(V) = c (V - V_{\text{th}}) \tag{3}
$$

where  $V_{\text{th}}$  is the threshold voltage determined by the position of the conduction band edge, and  $c$  is the capacitance per unit area. From the discussion above we conclude that after the delay time the local potential *V* follows a simple exponential law for the charging of a capacitor  $V(t) = V_g(1 - \exp(-t/\tau))$ . Combining this with Equation (2) and (3) we expect a time evolution for the conductivity

$$
\sigma(t) = \sigma_{\infty} \left( \alpha - \exp(-t/\tau) \right)^{4/3} \tag{4}
$$

Here,  $\sigma_{\infty} = A(cV_g)^{4/3}$ , and  $\alpha = 1 - V_{th}/V_g - n_c/(cV_g)$  is a con-<br>stant of order unity. This functional form fits the observed time stant of order unity. This functional form fits the observed time dependence very well, as demonstrated by the green solid curves in Figure 4.

The close match between the fit and the observed time dependence of the conductance supports the interpretation that the conductance is just controlled by the local electrostatic potential of the ionic liquids at the surface.

For 2D percolative systems such as random-resistor-tunneling networks,[27] certain I (V) characteristics are expected. When a voltage is applied between the source and drain electrodes, no current flows below a critical voltage  $V_c$ . For  $V > V_c$  a certain scaling behavior is to be expected, according to  $I_{sd} \propto (V - V_c)^{\delta}$ ,<sup>[28]</sup> for which the value  $\delta$  corresponds to the same 4/3<sup>[27]</sup> also found in which the value  $\delta$  corresponds to the same  $4/3$ ,<sup>[27]</sup> also found in  $SrTiO<sub>3</sub>$ .<sup>[29]</sup> We find similar, nonlinear behavior at low temperatures for gated channels (**Figure 7**). The channel gated at  $V_g$  = 2.5 V shows scaling behavior with  $\delta = 1.30 \pm 0.02$  and  $V_c = 1.2$ V at  $T = 20$  K.

Any electrochemical influence of the surface charge density would involve diffusion of the reaction species toward and away from the surface and is expected to show up as a time dependence *t*<sup>−1/2</sup>.<sup>[16,30</sup>] This would be seen as a lack of saturation at long times,<br>which we encounter in our experiments when we relay the conwhich we encounter in our experiments when we relax the conditions for treatment of the ionic liquid, so that oxygen (or water)



**Figure 7.** *I*(*V*) of the channel for  $V_g = 2.5$  V taken at  $T = 20$  K. The curve is nonlinear and can be described by the RRTN percolative model, where  $I_{sd} \propto (V_{sd} - V_c)^{\delta}$ . The inset shows the corresponding log–log plot of the negative *V*sd part of the data. The red, broken line shows the fit to the data with  $V_c = 1.2$  V, and  $\delta = 1.30 \pm 0.02$ .

contamination starts playing a role. For example, when evacuating the sample space at the start of the experiments to only 1 Pa we observe a break down of the scaling in Figure 5 and the 2DES conductivity does not saturate. Remarkably, poorer vacuum conditions often lead to higher maximum 2DES conductivities, but for longer gating times the conductivity starts degrading and becomes dependent on the gating history.

Apart from the methods employed in this work to illustrate the difference between electrostatics and electrochemistry, other methods can also be employed to further illustrate this contrast through enhancement of electrostatic gating. An example of this is the usage of monolayer separator layers such as hexagonal boron nitride<sup>[12]</sup> or other insulating materials to prevent direct contact between the ionic liquid and the surface of SrTiO3. Although the increased double layer distance limits the polarization and changes the Coulomb scattering and mobility properties of the 2DES, measurements of the onset of conduction with such a separator layer in place could provide a further means of minimizing any possible contribution of electrochemistry to the gating process here.

In the analysis of the time dependence, we have ignored delays in charging times due to the finite conductivity of the 2DES. In analyzing time delays similar to those reported here for experiments on  $SrTiO<sub>3</sub>$  with a solid electrolyte Tsuchiya et al.<sup>[18]</sup> proposed a model of charge build up that evolves gradually over the surface of the solid, initiated from the contact electrodes. Initially, the SrTiO<sub>3</sub> surface is assumed to be a perfect insulator. Near the electrodes the build up of an electrostatic potential in the ionic liquid pushes the chemical potential to the conduction band edge, allowing local charging of the  $SrTiO<sub>3</sub>$  surface. This charged surface then serves as the contact electrode for the next section of the surface.

If this model were correct the time delay would depend linearly on the channel length. We have tested this in two ways. First, for fixed channel length in Hall bar configuration we measured the conductivity across the channel at two points, using small capacitors to connect to the side contacts and an ac lock-in resistance measurement technique. The coupling by small capacitors prevents the side contacts from forming nucleation points for the formation of a 2DES. When switching the dc source–drain bias we find that the conductance along the channel and at two points across the channel develop nearly simultaneously. The noise on the ac signal limits our accuracy in determining the delay times, but they are equal to within 20%. In addition, we tested on another sample a series of channels in two-point configuration with lengths of 10, 20, 50, 100, 200, and 500  $\mu$ m. The observed time delays show a variation by about 50%, but no systematic systematic dependence. In fact, the shortest channel showed the longest delay time. We conclude that the charging of the surface occurs nearly homogeneously, and we tentatively attribute the small variation in delay times to variation in the geometries and positions on the sample.

The absence of a length dependence in the delay times suggests that the conductivity of the 2DES is not a limiting factor in the formation of the EDL. We picture the charging process as follows. When a gate potential is applied a standard EDL forms at the surfaces of the gate and at the Au contact electrodes. Since the area of the gate is much larger, the voltage drop concentrates at the surface of the small source and drain contacts. Toward equilibrium the potential would assume a homogeneous level inside the ionic liquid, and this would approach the potential of the gate. Since the surface of the  $SrTiO<sub>3</sub>$  crystal is in contact with this ionic liquid it feels the rising of the potential of the ionic liquid toward that of the gate over its entire surface. Only when this potential brings the conduction band edge to the level of the Fermi energy the EDL starts to form, and electrons flow in from the electrodes. At that moment the conductance is already well above the conductance of bulk  $SrTiO<sub>3</sub>$  in equilibrium. When the resistance of the 2DES drops below our detection limit of about 25 G $\Omega$ , the estimated RC time for a channel of  $10 \times 20 \,\mu$ m, using the quoted specific capacitance of 13  $\mu$ F cm<sup>−2</sup>, is only 0.6 s, much smaller than the observed time delays. We conclude that the observed time dependence is entirely dominated by the ionic conductivity of the IL.

In conclusion, we have demonstrated that IL gating on insulators, that is,  $SrTiO<sub>3</sub>$ , at charging temperatures close to the glass temperature  $T_g$  produces a delayed transition to a conducting surface state. The delay time is strongly dependent on the temperature and diverges near *T<sub>g</sub>*. The time delay and time evolution of the conductance can be described by a process of homogeneous charging of the surface, and is determined by the bandgap of the insulator. The conductance that results is consistent with a percolation model of transport. We provide a method of distinguishing between electrochemical and electrostatic processes by using the scaling behavior of the gate current, which is of crucial importance in this rapidly developing field of research. We find no evidence of charge doping by electrochemical reactions when charging close to the glass temperature of the IL, provided we maintain strict conditions during handling of the ionic liquid. Further verification of the dominance of electrostatic gating could be achieved by experiments employing an atomically thin separation layer, such as boron nitride.<sup>[13]</sup>

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## **Conflict of Interest**

The authors declare no conflict of interest.

## **Keywords**

electric double layer, ionic liquids, nanoionics, SrTiO<sub>3</sub>, two-dimensional conductors

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